

Fig.1

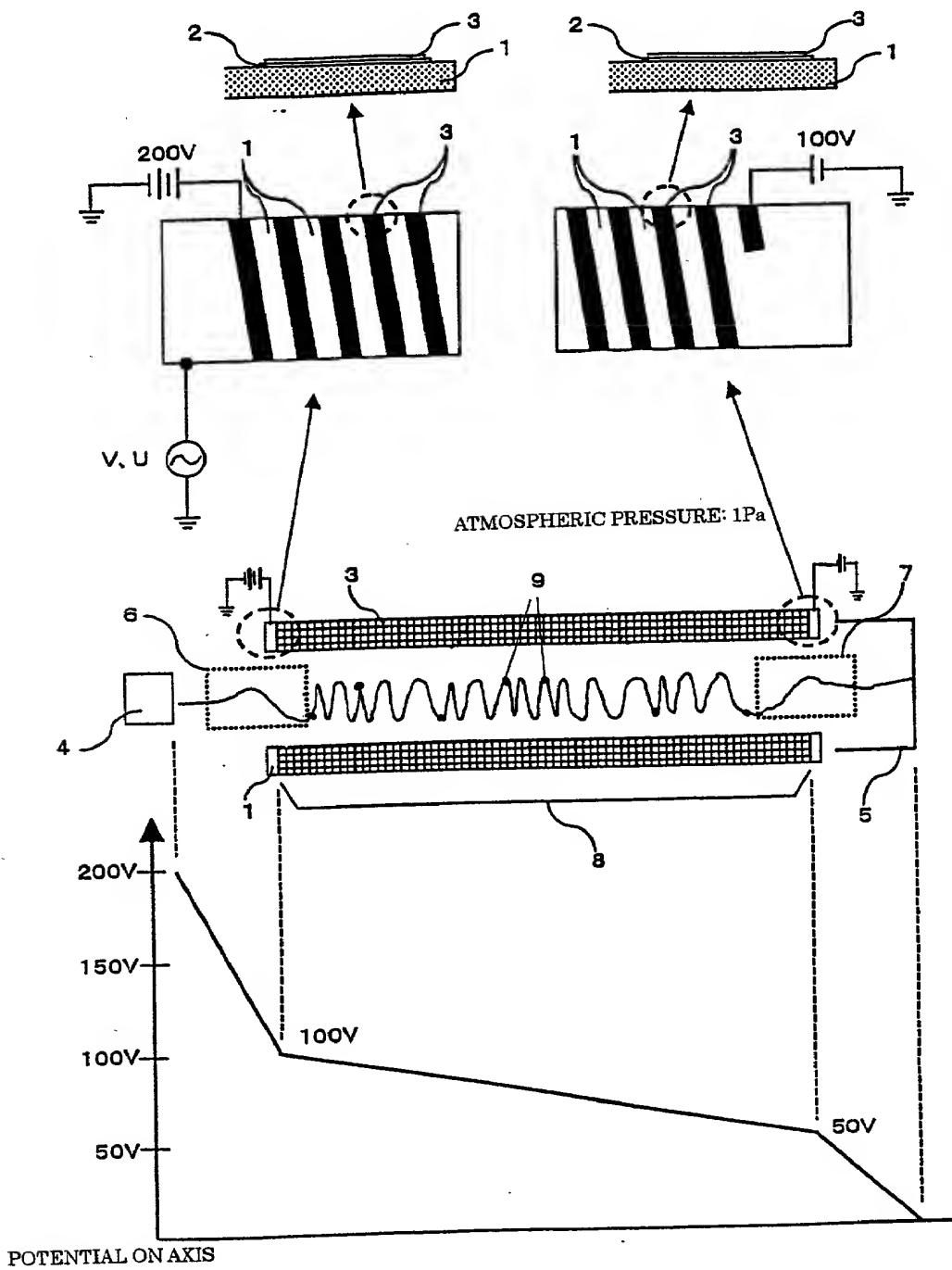


Fig. 2

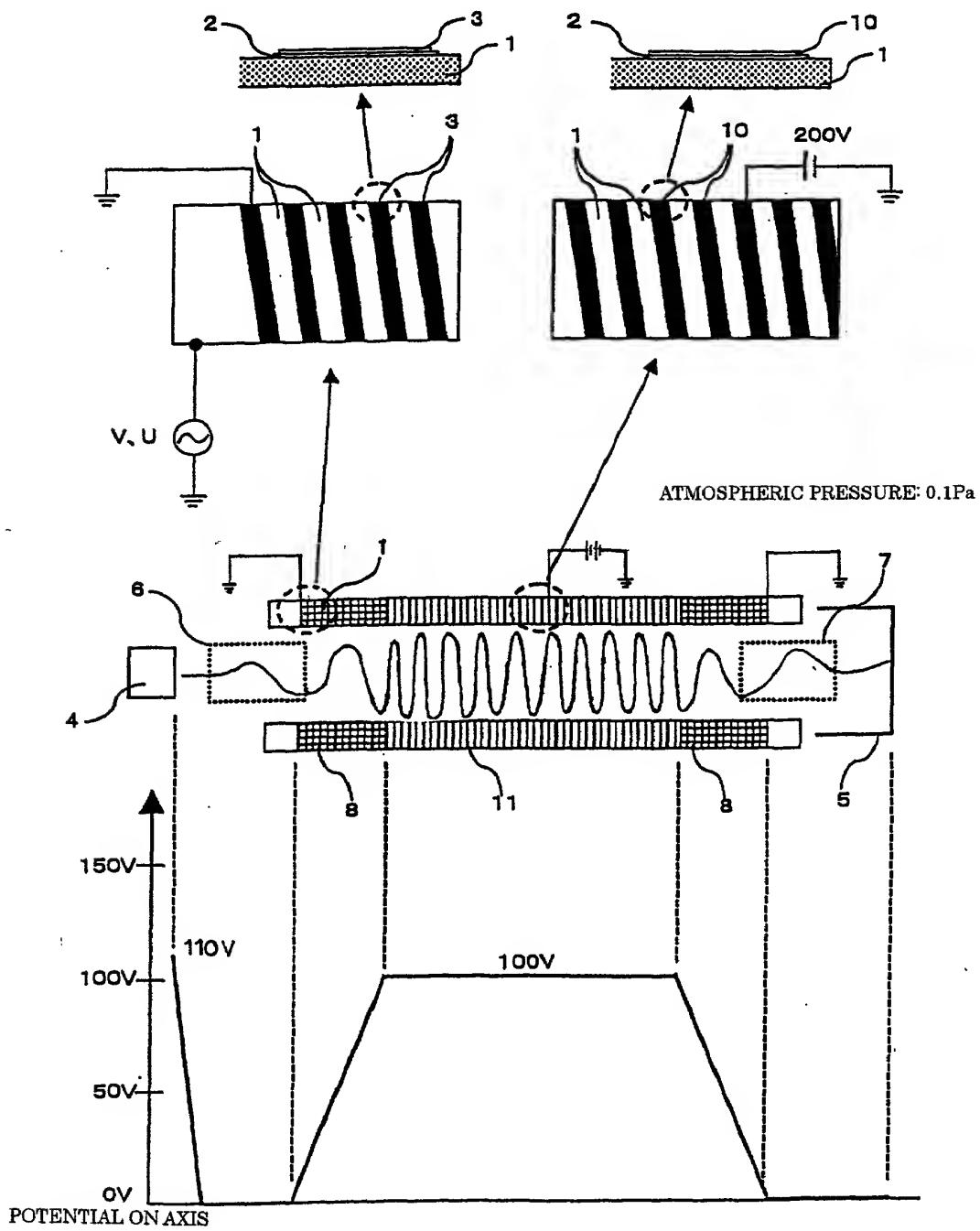


Fig.3(a)

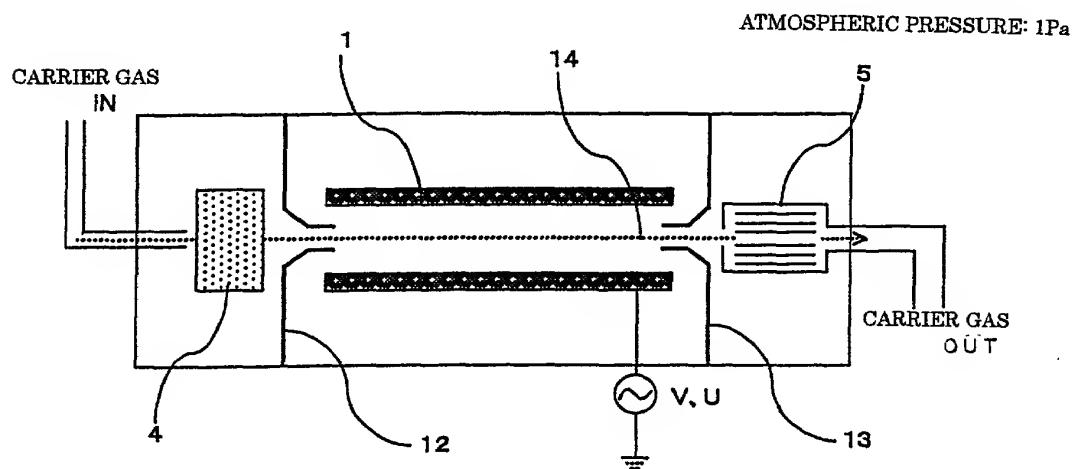


Fig.3(b)

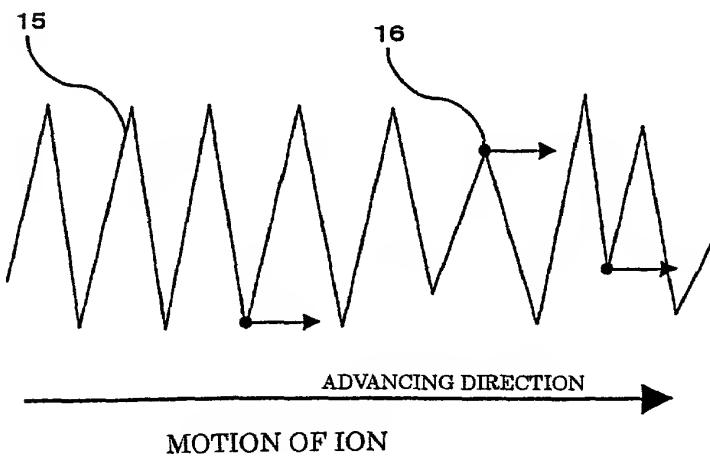


Fig.4

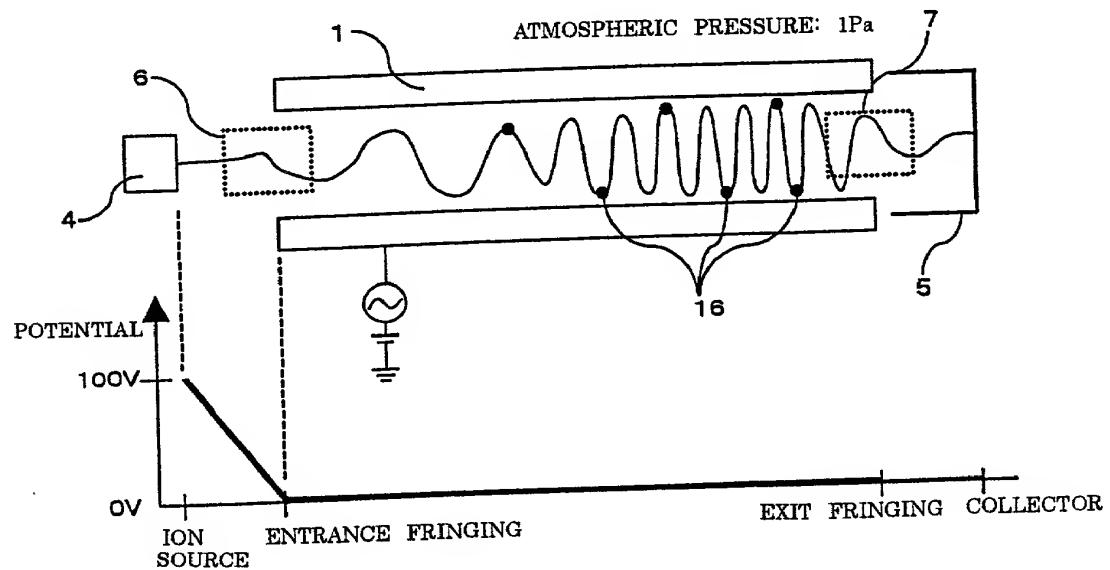


Fig.5

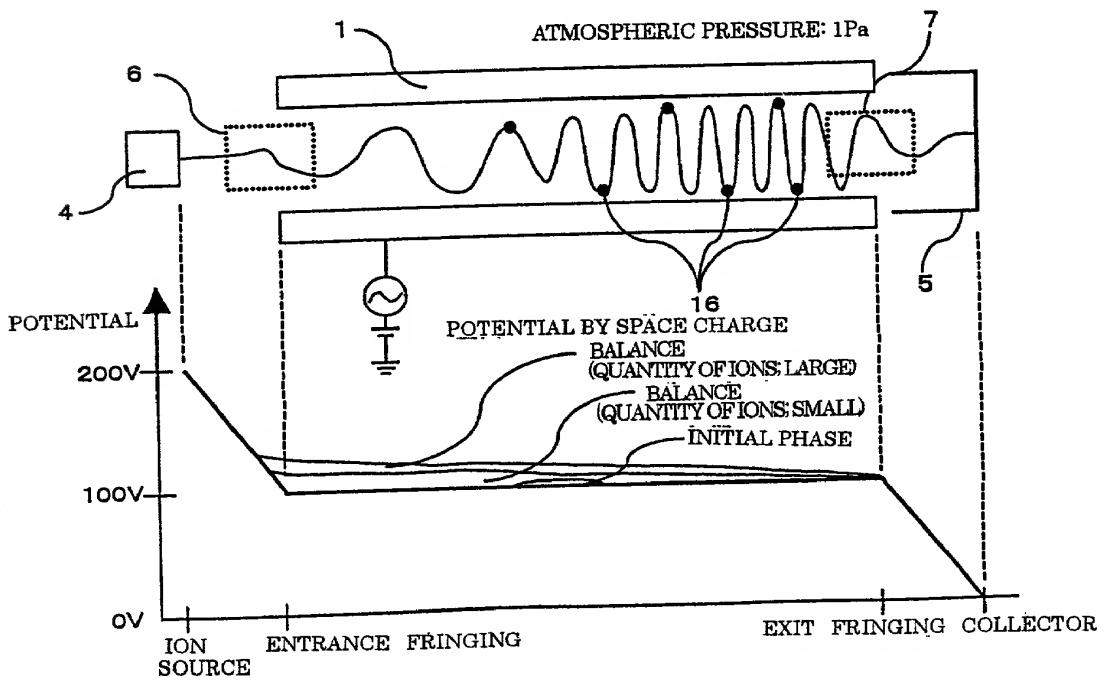


Fig.6

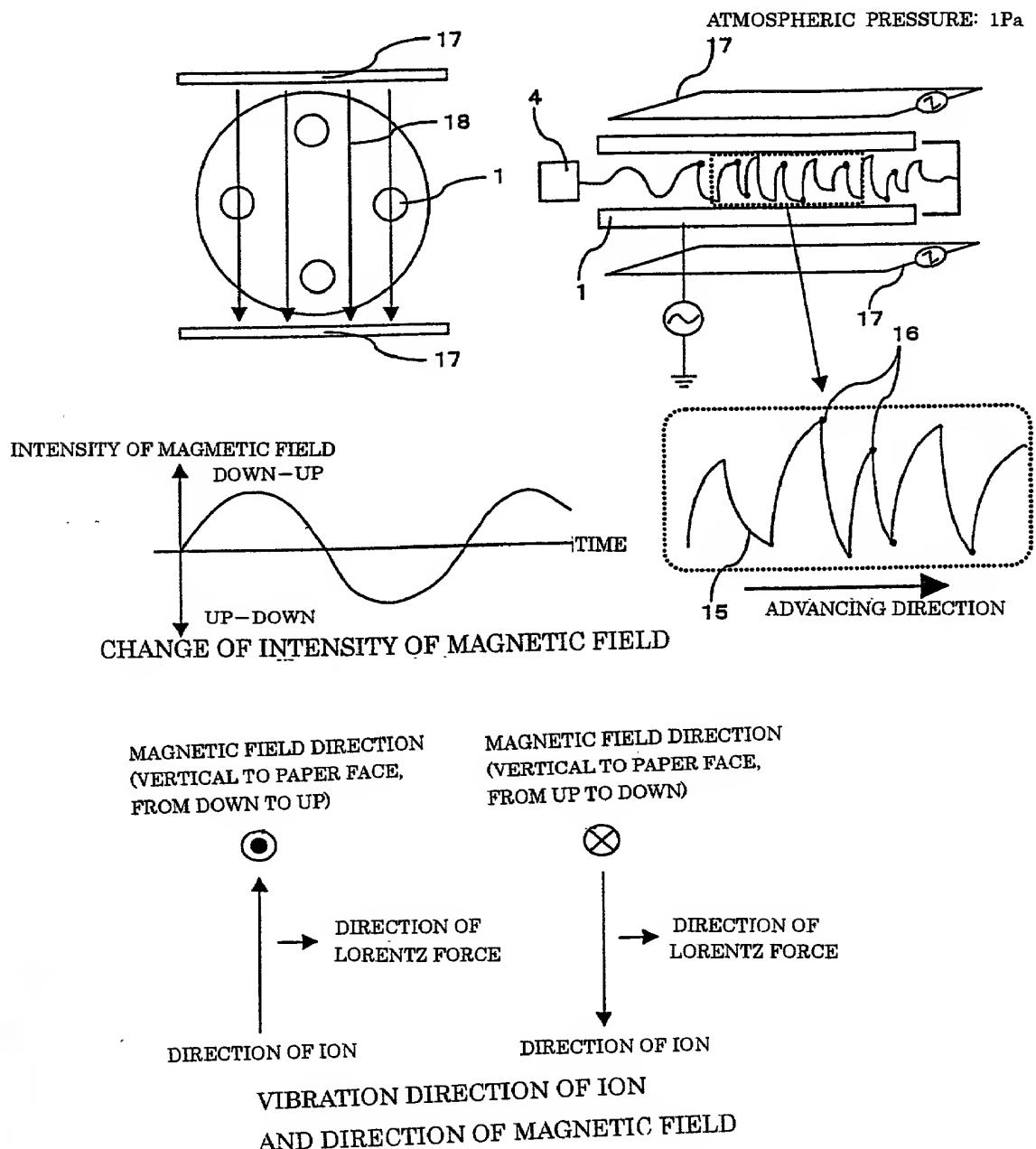


Fig.7

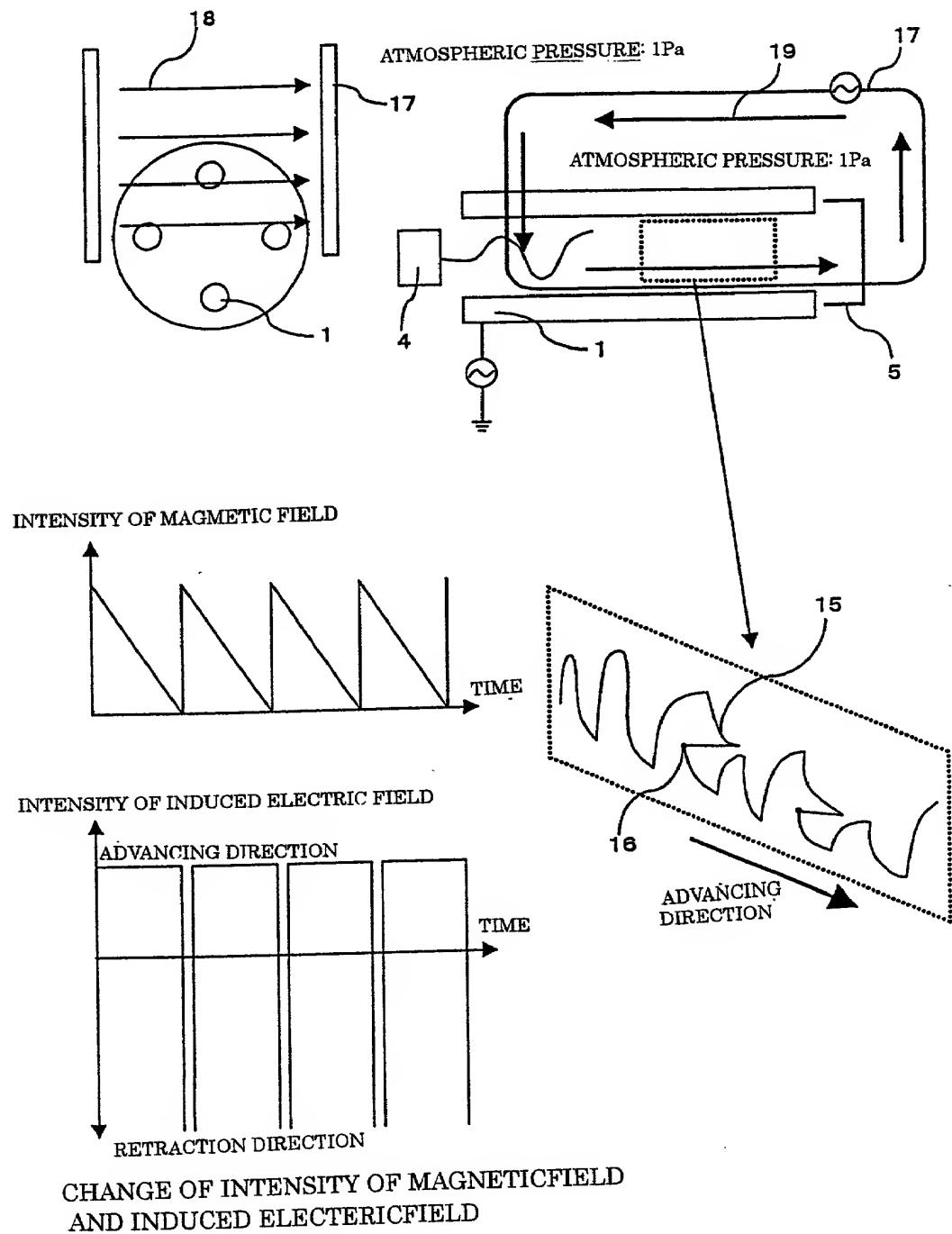


Fig.8(a)

ACCUMULATION MODE

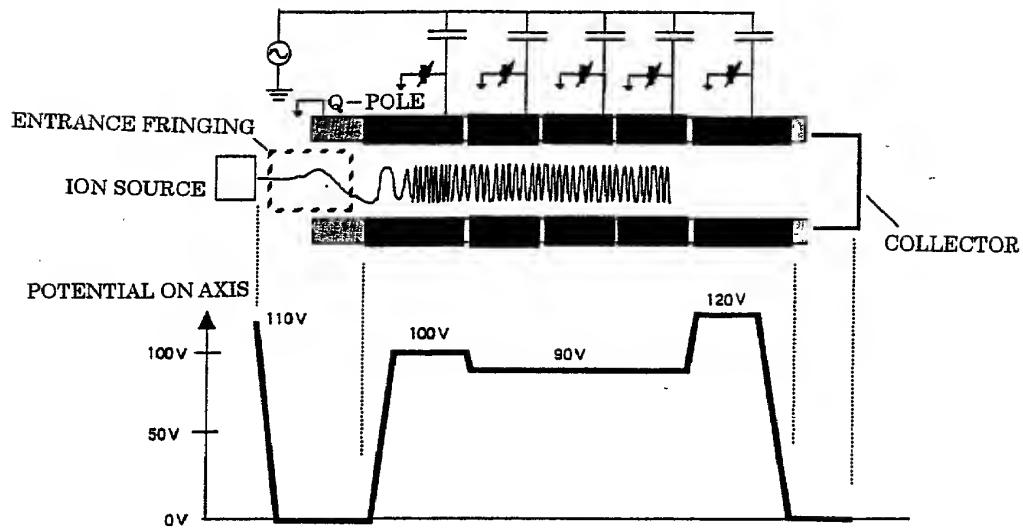


Fig.8(b)

DETECTION MODE (INTERMITTENT OPERATION)

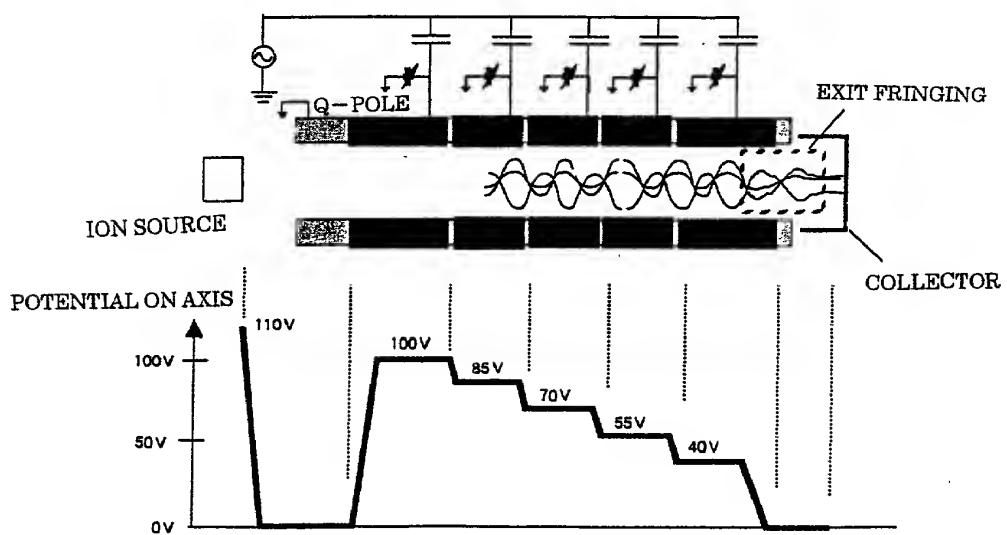


Fig.9(prior art)

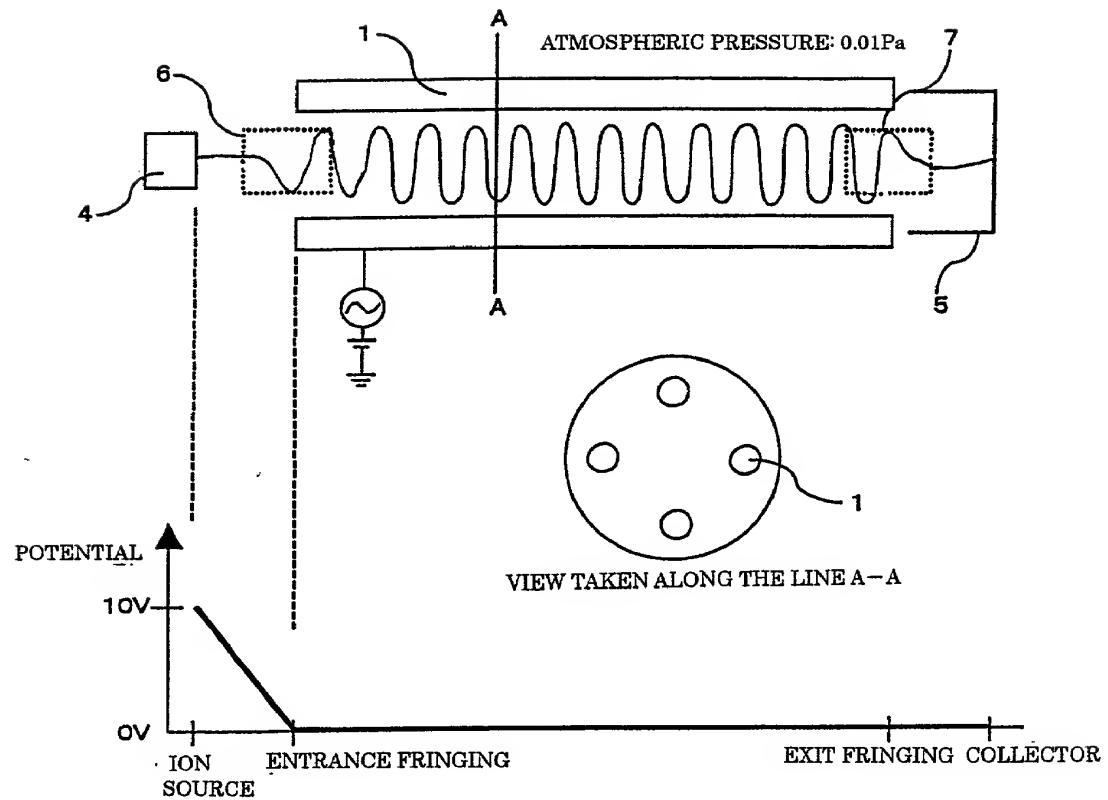


Fig.10(prior art)

